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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/813,236	03/31/2004	Kenichi Yoshimochi	AI 331	3883	
7:	590 10/05/2005		EXAM	INER	
RABIN & BERDO, P.C.			VU, HUNG K		
Suite 500 1101 14 Street,	N.W.		ART UNIT	PAPER NUMBER	
Washington, DC 20005			2811		
		DATE MAILED: 10/05/2005			

Please find below and/or attached an Office communication concerning this application or proceeding.

				AK			
		Application No.	Applicant(s)				
Office Action Summary		10/813,236	YOSHIMOCHI, KENIC	HI			
		Examiner	Art Unit				
		Hung Vu	2811				
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the	correspondence addres	SS			
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
2a)	Responsive to communication(s) filed on This action is FINAL. 2b) This Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pr		erits is			
Dispositi	ion of Claims						
4) ☐ Claim(s) 1-6 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-4 and 6 is/are rejected. 7) ☐ Claim(s) 5 is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or election requirement.							
Applicati	ion Papers	•					
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 							
Priority ι	under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
2) Notice	t(s) te of References Cited (PTO-892) te of Draftsperson's Patent Drawing Review (PTO-948) mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) ter No(s)/Mail Date 03/31/04.	4) Interview Summar Paper No(s)/Mail D 5) Notice of Informal 6) Other:		2)			

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DETAILED ACTION

Specification

1. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Objections

2. Claims 2-6 are objected to because of the following informalities: In claims 2-6, line 1, "A" should be changed to "The" for clarity. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Shibata et al. (US 2002/0104988).

Shibata et al. discloses, as shown in Figures 1-6, a semiconductor device comprising:

a channel region (4) of a first conductivity (p) provided in a surface of a semiconductor substrate (1);

a source region (5) of a second conductivity (n) different from the first conductivity, the source region being provided on an edge of a trench (6) which extends through the channel region;

a gate oxide film (7) provided on an interior wall of the trench; and

a gate electrode (8) provided in the trench in opposed relation to the channel region with the intervention of the gate oxide film;

wherein the interior wall of the trench includes a first interior side surface having a (100) plane orientation, and a second interior side surface having a plane orientation different from the plane orientation of the first interior side surface [see Figs. 3A-3B and 5A-5B];

wherein the source region is disposed away from a portion of the gate oxide film provided on the second interior side surface.

Regarding claim 2, Shibata et al. discloses the interior wall of the trench further include a bottom surface having a major plane orientation with a higher areal atom density than the first interior side surface [see Figs. 3A-3B and 5A-5B].

Regarding claim 3, Shibata et al. discloses the surface of the semiconductor substrate has plane orientation with greater areal atom density than the surface having the (100) plane orientation [see Figs. 3A-3B and 5A-5B].

Regarding claim 4, Shibata et al. discloses wherein the surface of the semiconductor substrate has a (110) plane orientation [Col. 3, lines 27-30].

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Regarding claim 6, Shibata et al. discloses the first interior side surface has a greater length as measured along the surface of the semiconductor substrate than the second interior side surface [see Figs. 1,2,4 and 6].

Allowable Subject Matter

- 4. Claim 5 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 5. The following is an examiner's statement of reasons for allowance:

Applicant's claim 5 is allowable over the references of record because none of these references disclose or can be combined to yield the claimed device further comprising a lower resistance region of the first conductivity extending longitudinally of the trench portions and intervening between the second interior side surface and the source region, the lower resistance region being imparted with a lower resistance by introduction of an impurity, as recited in claim 5 in combination with the remaining claimed limitations.

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung K. Vu whose telephone number is (571) 272-1666. The examiner can normally be reached on Tuesday-Friday 6:00-4:30, Eastern Time.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's acting supervisor, Steven Loke can be reached on (571) 272-1657. The Central Fax Number for the organization where this application or proceeding is assigned is (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

 $V\mathfrak{u}$

September 30, 2005

Hung Vu

Primary Examiner